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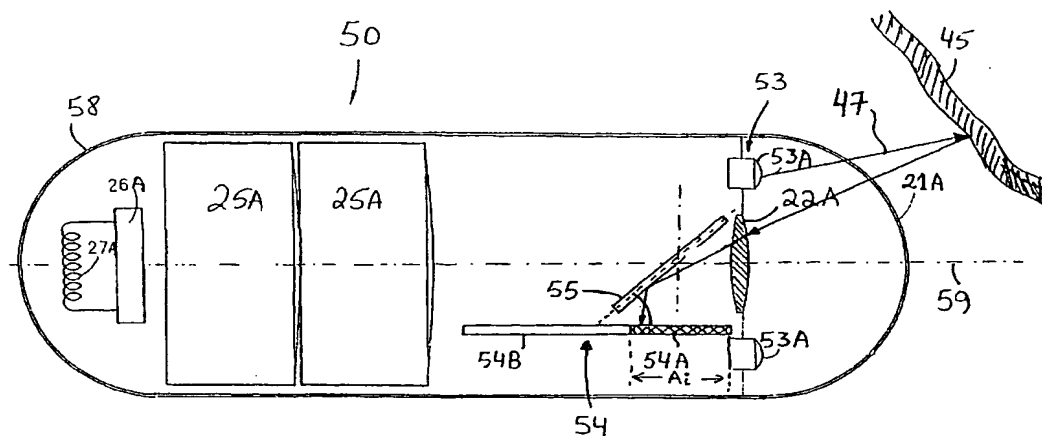
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- (71) Applicant (for all designated States except US): **GIVEN IMAGING LTD.** [IL/IL]; HaCarmel St. 2, Industrial Park, 20692 Yoqneam (IL).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): **GLUKHOVSKY, Arkady** [IL/IL]; Hanuriot Street 24/5, 36790 Nesher (IL). **MERON, Gavriel** [IL/IL]; Weizman Street 21, 49556 Kfar Ganim, Petach Tikva (IL).
- (74) Agents: **PEARL, Zeev** et al.; Eitan, Pearl, Latzer & Cohen-Zedek, Gav Yam Center 2, Shenkar Street 7, 46725 Herzlia (IL).
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(54) Title: **IN VIVO IMAGING DEVICE WITH A SMALL CROSS SECTIONAL AREA AND METHODS FOR CONSTRUCTION THEREOF**



(57) Abstract: An image sensor (e.g., a CMOS sensor) includes a pixel array portion and a circuitry portion, where the circuitry portion may be segregated, for example longitudinally, from the pixel array portion.

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IN VIVO IMAGING DEVICE WITH A SMALL CROSS SECTIONAL AREA AND METHODS FOR CONSTRUCTION THEREOF

BACKGROUND OF THE INVENTION

5 Devices and methods for performing in-vivo imaging of passages or cavities within a body are known in the art. Such devices may include, inter alia, various endoscopic imaging systems and devices for performing imaging in various internal body cavities.

Reference is now made to Fig. 1 which is a schematic diagram illustrating
10 an embodiment of an autonomous in-vivo imaging device. The device 10A typically includes a capsule-like housing 18 having a wall 18A. The device 10A has an optical window 21 and an imaging system for obtaining images from inside a body cavity or lumen, such as the GI tract. The imaging system may include an illumination unit 23. The illumination unit 23 may include one or more light sources
15 23A. The one or more light sources 23A may be a white light emitting diode (LED), or any other suitable light source, known in the art. The imaging system of the device 10A may include an imager 24, such as a CMOS or CCD, which acquires the images and an optical system 22 which focuses the images onto the imager 24. Typically, the imager 24 is arranged so that its surface 27 is
20 perpendicular to the longitudinal axis 19 of the device 10A. The illumination unit 23 illuminates the inner portions of the body lumen through an optical window 21. Device 10A further includes a transmitter 26 and an antenna 27 for transmitting the image signal of the imager 24, and one or more power sources 25. The power
25 source(s) 25 may be any suitable power sources such as but not limited to silver oxide batteries, lithium batteries, or other electrochemical cells having a high energy density, or the like. The power source(s) 25 may provide power to the electrical elements of the device 10A.

Typically, in the gastrointestinal application, as the device 10A is transported through the gastrointestinal (GI) tract, the imager, such as but not
30 limited to a multi-pixel CMOS imager acquires images (frames) which are processed and transmitted to an external receiver/recorder (not shown) worn by

the patient for recording and storage. The recorded data may then be downloaded from the receiver/recorder to a computer or workstation (not shown) for display and analysis. During the movement of the device 10A through the GI tract, the imager may acquire frames at a fixed or at a variable frame acquisition rate. For example, in one embodiment the imager (such as, but not limited to a CMOS imager) may acquire images at a fixed rate of two frames per second (2 Hz). However, other different frame rates may also be used, depending, inter alia, on the type and characteristics of the specific imager or camera or sensor array implementation which is used, and on the available transmission bandwidth of the transmitter 26. The downloaded images may be displayed by the workstation by replaying them at a desired frame rate. This way, the expert or physician examining the data is provided with a movie-like video playback which may enable the physician to review the passage of the device through the GI tract.

It may generally be desirable to decrease the size and particularly the cross sectional area of in vivo imaging devices, such as the device 10A of Fig. 1, or of imaging devices that are to be inserted into working channels of endoscope-like devices, or integrated into catheter-like devices which may be used in conjunction with guide wires, or the like. Smaller catheter like devices with reduced area may be inserted into narrower body cavities or lumens, such as for example, the coronary arteries, the urethra, the common bile duct, or the like and may also be easier to insert into working channels of other devices such as endoscopes, laparoscopes, gastroscopes, or the like.

Decreasing the cross-sectional area of such devices may be limited by the cross-sectional area of the imaging sensor, such as for example the imager 24 of Fig. 1

SUMMARY OF THE INVENTION

In one embodiment, an image sensor (e.g., a CMOS sensor) includes a pixel array portion and a circuitry portion, where the circuitry portion may be segregated, for example longitudinally, from the pixel array portion.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention is herein described, by way of example only, with reference to the accompanying drawings, in which like components are designated by like reference numerals, wherein:

Fig. 1 is a schematic diagram illustrating an embodiment of a prior art autonomous in-vivo imaging device;

Fig. 2 is a schematic front view of the surface layout of a typical prior art CMOS imager.

Fig. 3 is a schematic front view of the imager 24 of Fig.1;

Fig. 4 is a schematic cross sectional view illustrating a CMOS imager having a square cross-sectional area disposed within a housing having a circular cross-section;

Fig. 5 is a schematic top view showing the layout of a rectangular CMOS imager having segregated pixel array area and support circuitry areas, in accordance with a preferred embodiment of the present invention;

Fig. 6 is a cross-sectional view illustrating an in-vivo imaging device having a reduced cross-sectional area and including the CMOS imager of Fig. 5, in accordance with a preferred embodiment of the present invention;

Fig. 7 is a cross-sectional view illustrating an in-vivo imaging device having a reduced cross-sectional area and including the CMOS imager of Fig. 5, in accordance with another preferred embodiment of the present invention; and

Fig. 8 is a schematic cross sectional view of part of an insertable device for in vivo imaging, in accordance with a preferred embodiment of the present invention.

DETAILED DESCRIPTION OF THE INVENTION

In the following description, various aspects of the present invention will be described. For purposes of explanation, specific configurations and details are set forth in order to provide a thorough understanding of the present invention. However, it will also be apparent to one skilled in the art that the present invention may be practiced without the specific details presented herein. Furthermore, well known features may be omitted or simplified in order not to obscure the present invention.

The present invention is based on providing an imager having a small cross-sectional area for implementing in vivo imaging devices such as, but not limited to, swallowable autonomous in-vivo imaging devices (capsule-like or shaped otherwise), and wired or wireless imaging units which are integrated into endoscope-like devices, catheter-like devices, or any other type of in-vivo imaging device that can be introduced into a body cavity or a body lumen.

It is noted that while the embodiments of the invention shown hereinbelow are adapted for imaging of the gastrointestinal (GI) tract, the devices and methods disclosed may be adapted for imaging other body cavities or spaces.

Fig. 2 is a schematic front view of the surface layout of a typical prior art CMOS imager. The CMOS imager 30 is an integrated circuit, which is typically implemented on a silicon wafer. The CMOS imager 30 includes an imaging sensor part 30A which is usually located at one of the corners of the frontal surface of the CMOS imager 30. The imaging sensor part 30A may include the two dimensional array of light sensitive diodes (not shown in detail) comprising the sensor pixels (not shown in detail), and may also include integrated amplification circuitry (not shown) and switching circuitry (not shown) for controlling the pixel sampling or readout, and may also include electrical conducting paths for connecting the pixels to the functional units that perform the readout of the pixels.

The remaining surface portion 30B of the CMOS imager 30, may include, inter alia, integrated circuitry (not shown in detail) for performing various control and timing functions, analog to digital (A/D) conversion circuitry (not shown) for converting the analog signal sampled from the individual pixels and various input/output (I/O) circuitry (not shown in detail) for sending the image digitized data and control signals as output signals to devices or circuitry (not shown) such as but not limited to a transmitter like the transmitter 26 of Fig. 1, or any other signal processing circuit or element or unit which may be connected to the CMOS imager 30, as is known in the art.

Reference is now made to Fig. 3 which is a schematic front view of the imager 24 of Fig. 1. The imager 24 of Fig. 1 may be a CMOS imager and may be configured to have a square shape having a side length S (as seen in the front view of Fig 3).

The CMOS imager 24 may include an imaging sensor part 24A which may be typically located at the center of the frontal surface of the CMOS imager 24. The imaging sensor part 24A may include the two dimensional array of light sensitive diodes (not shown in detail) comprising the sensor pixels (not shown in detail), and may also include integrated amplification circuitry (not shown) and switching circuitry (not shown) for controlling the pixel sampling or readout, and may also include electrical conducting paths for connecting the pixels to the functional units that perform the readout of the pixels.

The remaining surface portion 24B of the of the CMOS imager 24, which surrounds the imaging sensor part 24A, may include, inter alia, integrated circuitry (not shown in detail) for performing various control and timing functions, analog to digital (A/D) conversion circuitry (not shown) for converting the analog signal sampled from the individual pixels and various input/output (I/O) circuitry (not shown in detail) for sending the image digitized data and control signals as output signals to devices or circuitry (not shown) such as but not limited to a transmitter like the transmitter 26 of Fig. 1, or any other signal processing circuit or element or unit which may be connected to the CMOS imager 24.

The advantage of the imager configuration shown in Fig. 3, is that it makes possible to position the optical system 22 (best seen in Fig. 1) in the

center of the optical window 21 (see Fig. 1) without unduly increasing the cross-sectional area of the entire device 10A of Fig. 1). However, with the configuration (shown in Fig. 3) of the CMOS imager 24, if it is desired to minimize the cross-sectional area of the device 10A, the diminishing of the cross-sectional area of the entire device 10A may be limited by the diagonal of the entire CMOS imager 24.

Reference is now made to Fig. 4 which is a schematic cross sectional view illustrating a CMOS imager having a square cross-sectional area disposed within a housing having a circular cross-section.

The CMOS imager 24 is shown disposed within a housing 28 having a wall 28A. The housing 28 has a circular cross-section. The wall 28A may be part of a capsule like housing, such as the housing 18 shown in the capsule-like device 10A of Fig. 1). The wall 28A may also be the wall of an imaging unit which comprises part of an elongated device such as a catheter-like device (not shown), or an endoscope-like device (not shown) or the like.

Similar to the CMOS imager 24 of Fig. 3, the square CMOS imager 34 of Fig. 4 may have a square imaging sensor part 34A (having a side length of A_i) including the imaging pixels (the pixels are not shown in detail) and a remaining surface part 34B which surrounds the imaging sensor part 34A and which may include all the supporting circuitry (not shown) as disclosed in detail hereinabove for the surface part 24B of Fig. 3. The entire CMOS imager 34 has a side S , and diagonal D . It can be seen from Fig. 4 that the internal diameter of the wall 28A of the housing 28 may not be smaller than the length of the diagonal D of the square CMOS imager 34. Thus, using the configuration of the CMOS imager 34, it is not possible to decrease the diameter of the housing 28 to a length smaller than the diagonal D of the entire CMOS imager 34.

Reference is now made to Fig. 5 which is a schematic top view showing the layout of a rectangular CMOS imager having segregated pixel array area and support circuitry areas, in accordance with a preferred embodiment of the present invention. The CMOS imager 54 may include a square imaging sensor part 54A (having a side length of A_i) including the imaging pixels (the pixels are

not shown in detail) and a remaining surface part 54B which is longitudinally segregated from the imaging sensor part 54A and which may include all the necessary supporting circuitry (not shown) as disclosed in detail hereinabove.

5 The length L_i of the CMOS imager 54 is adapted such that all the necessary supporting circuitry may be accommodated in the part 54B. Thus, when the CMOS imager 54 is compared to the CMOS imager 34 of Fig. 4, the dimensions of the imaging sensor parts 34A and 54A are the same (both imaging sensor parts may be a square having a side length of A_i), the CMOS imager 34 has a square shape having a side S , while the CMOS imager 54 has
10 an elongated shape having a length L_i .

Reference is now made to Fig. 6 which is a cross-sectional view illustrating an in-vivo imaging device having a reduced cross-sectional area and including the CMOS imager of Fig. 5, in accordance with a preferred embodiment of the present invention.

15 The device 50 includes a capsule-like housing 58 which has an optical window 21A. The device 50 may include an optical system 22A and a mirror 55. The device 50 may include the CMOS imager 54 of Fig. 5. The mirror 55 may be substituted by any suitable light deflecting element such as a suitably configured prism, or the like, for deflecting the light rays collected by the 22A
20 optical system towards the part 54A of the CMOS imager 54. The optical system 22A may be a lens, a group of lenses, a zoom lens, a composite lens, a wide angle lens or any other suitable image forming optical element known in the art.

The device 50 further includes the power sources 25A which may be
25 similar to the power sources 25 of Fig. 1 but may be smaller in size due to the reduced cross sectional area of the device 50. The device 50 also includes an illuminating unit 53 comprising the light sources 53A. The construction and operation of the illuminating unit 53 and the light sources 53A may be as disclosed in detail for the illuminating unit 23 and the light sources 23A of Fig. 1,
30 and possibly, as disclosed in WO 01/65995.. The device 50 may also include a wireless transmitter 26A and an antenna 27A for transmitting the data of the acquired images.

The CMOS imager 54 is disposed longitudinally within the housing 58 such that the light rays 47 generated by the light sources 53A are reflected from the intestinal wall and pass through the optical window 21A. The reflected light rays are collected by the optical system 22A and are deflected towards the part 54A of the CMOS imager 54 to create an image to be sensed by the light sensing pixels (not shown) included in the part 54A. Preferably, but not necessarily, the CMOS imager 54 is disposed such that its longitudinal axis (not shown) is aligned parallel to the longitudinal axis 59 of the device 50. The angle α between the surface of the mirror 55 and the surface of the CMOS imager may be 45° , but may also be smaller than 45° . If the angle α is smaller than 45° , the image projected upon the pixels of the part 54A of the CMOS imager 54 may be distorted. Thus, the optical system 22A may be configured to suitably change the collected image in order to compensate for the distortion before the image reaches the part 54A of the CMOS imager 54. Alternatively, the distortion in the acquired image may be corrected after the acquisition by suitably processing the image data at a stage later than image acquisition. For example a distorted image may be processed in a workstation (not shown) after the image has been transmitted by the transmitter 26A. Such a distortion may be compensated by suitable computational algorithms, as is known in the art.

It is noted that in comparison to the configuration of the CMOS imaging unit 24 of Fig. 1 and the CMOS imaging unit 34 of Fig. 4 which are oriented perpendicular the longitudinal axis of the in vivo imaging device 10A, the configuration of the CMOS imager 54 being longitudinal and parallel to the axis 59 of the device 50 may enable a substantial reduction in the cross sectional area of the device 50 since the combination of the reduced area of the light sensitive part 54A and the use of the mirror 55 allow such a reduction in cross-sectional area. It is noted that the cross sectional area $(A_i)^2$ of the part 54A of Fig. 6 is substantially smaller than the cross sectional area $(D)^2$ of the entire CMOS imager 34 of Fig. 4. The cross-sectional area of a cross section taken in a direction perpendicular to the longitudinal axis the device 50 may therefore be substantially reduced in comparison to the cross sectional area of a

cross section taken in a direction perpendicular to the longitudinal axis 19 of the device 10A..

It is noted that additional configurations of the optical components of the in vivo imaging device may be possible.

5 Reference is now made to Fig. 7 which is a cross-sectional view illustrating an in-vivo imaging device having a reduced cross-sectional area and including the CMOS imager of Fig. 5, in accordance with another preferred embodiment of the present invention.

10 The device 60 includes a capsule-like housing 58 which has an optical window 21A. The device 60 may include an aperture 51, an optical system 65A and a mirror 55. The device 60 may include the CMOS imager 54 of Fig. 5. The mirror 55 may be substituted by any suitable light deflecting element such as a suitably configured prism (not shown), or the like, for deflecting the light rays 47C passing through by the aperture 51, towards the optical system 65A which
15 projects an image on the part 54A of the CMOS imager 54. The optical system 65A may be a lens, a group of lenses, a zoom lens, a composite lens, a wide angle lens or any other suitable image forming optical element known in the art.

 The device 60 further includes the power sources 25A which may be similar to the power sources 25A of Fig. 6 .

20 The CMOS imager 54 is disposed longitudinally within the housing 58 such that the light rays 47A and 47B generated by the light sources 53A are reflected from the intestinal wall 45 and pass through the optical window 21A as light rays 47C and 47D, respectively. The reflected light rays 47C and 47D are deflected by the mirror 55 towards the optical system 22A. The optical system
25 65A thus focuses an image on the part 54A of the CMOS imager 54 the image may be sensed by the light sensing pixels (not shown) included in the part 54A. Preferably, but not necessarily, the CMOS imager 54 is disposed such that its longitudinal axis (not shown) is aligned parallel to the longitudinal axis 69 of the device 60. The angle α between the surface of the mirror 55 and the surface of
30 the CMOS imager may be 45° , but may also be smaller than 45° .

 If the angle α is smaller than 45° , the image projected upon the pixels of the part 54A of the CMOS imager 54 may be distorted. Thus, the optical system

65A may be configured to suitably change the collected image in order to compensate for the distortion before the image reaches the part 54A of the CMOS imager 54. Alternatively, the distortion in the acquired image may be corrected after the acquisition by suitably processing the image data at a stage
5 later than image acquisition. For example, a distorted image may be processed in a workstation (not shown) after the image has been transmitted by the transmitter 26A. Such a distortion may be compensated by suitable computational algorithms, as is known in the art.

The configuration of the imager 54 and the mirror 55 and the optical
10 system 65A of the device 60 may also enable the device 60 to have a reduced cross sectional area for the reasons disclosed in detail hereinabove.

Thus, the combination of the CMOS imager having segregated imaging and support circuitry parts disclosed hereinabove, and the longitudinal arrangement of such a segregated CMOS imager within the device allow the
15 construction of autonomous or non-autonomous in vivo imaging devices with a small cross sectional area. The non-autonomous devices may include but are not limited to imaging heads or imaging units or imaging assemblies which are constructed as an integral part of, or are included within, or are attached to catheter like devices, endoscope-like devices, trocars, or any other type of
20 device which may be used for in vivo surgical and/or diagnostic purposes requiring imaging capabilities and may benefit from the reduced cross-sectional area of such imaging heads or imaging units or imaging assemblies.

Reference is now made to Fig. 8 which is a schematic cross sectional view of part of an insertable device for in vivo imaging, in accordance with a
25 preferred embodiment of the present invention.

The insertable device 80 may include an elongated (preferably flexible) housing 78. The housing 78 may have an optical window 21A sealingly attached at the end 80A thereof. The device 80 may further include the CMOS imager 54 which is arranged longitudinally within the housing 78. The CMOS
30 imager 54 has an imaging light sensitive part 54A segregated from other support circuitry part 54B as disclosed in detail hereinabove with respect to Figs. 5-7.

The device 80 may include a mirror 55 which is inclined at an angle α to the imaging part 54A of the CMOS imager 54, as disclosed hereinabove. The angle α may be equal to 45° or may be different than 45°. Making the angle α smaller than 45°, may enable further reduction of the diameter or the cross sectional area of the device 80 as disclosed hereinabove for the devices 50 and 60. An optical system 22C may be suitably aligned along the longitudinal axis 89 of the device 80. The optical system 22C may include a single lens, multiple lenses, or other suitable optical elements like filters, as disclosed in detail hereinabove for the optical systems 22, 22A and 65A of Figs. 1, 6 and 7, respectively.

The device 80 may include an illumination unit 23H which may include light sources 23B. The light sources 23B may be the white LED light sources disclosed in detail hereinabove, and possibly, as disclosed in WO 01/65995, but may also be any other suitable miniature light sources known in the art. The CMOS imager 54 may be connected to a suitable power source 85 by suitable electrically conducting wires 92 connected to the power source 85. The CMOS imager 54 may be connected to suitable electrically conducting wires 82 for transmitting the image data to an external device (not shown) for further processing and for displaying of the acquired images.

It is noted that in accordance with another preferred embodiment of the present invention, the image data may be transmitted wirelessly to a receiver or a receiver/recorder, as is disclosed in detail for the autonomous in vivo imaging device 10A, and possibly, as disclosed in WO 01/65995. In such a case the device 80 may include an internal power source, a wireless transmitter (such as but not limited to the transmitters 26 or 26A of Figs 1, 6 and 7) and an antenna (such as but not limited to the antenna 27 of the device 10A).

The light sources 23B may be connected to the power source 85 by suitable electrically conducting wires 90 disposed within the housing 78. Alternatively, the light sources 23B may receive power and may be controlled by being coupled to the CMOS imager 54 (connections are not shown).

The power source 85 may be any suitable electrical power source, including but not limited to a mains operated power source, a battery, or the like.

Alternatively, the power source 85 may be disposed within the housing 78 (not shown).

Other alternative designs of the optical arrangement in the device 80 may also be used. For example, the device 80 may have an optical aperture (such as, for example, the optical aperture 51 of Fig. 7) and an optical system (such as, for example, the optical system 65A of Fig. 7) disposed between the part 54A of the CMOS imager 54 and the mirror 55.

If the angle α is smaller than 45° , and a correction is needed for the distortion in the image, the distortion may be corrected by the optical system 22C (or by the optical system disposed between the part 54A of the CMOS imager 54 and the mirror 55, if the alternative optical arrangement is being used). Alternatively, image distortion may be corrected computationally by suitably processing the image data in a post-acquisition step, as is known in the art.

The segregated circuitry design of the CMOS imager 54 and the use of the mirror 55, enable the reduction of the transverse cross sectional area of the device 80 as explained in detail hereinabove for the devices 50 and 60 of Figs. 6 and 7 respectively.

It is noted that, for the sake of simplicity of illustration, the insertable device 80 is illustrated as having imaging capabilities only. The device 80 may for example be inserted into a working channel of an endoscope (not shown) and may be used for insertion into and imaging of narrow cavities into which the endoscope cannot be inserted. However, The device 80 itself may include one or more working channels (not shown in Fig. 8 for the sake of clarity of illustration). The working channels may be used for performing insufflation and/or irrigation as is known in the art. Additionally, one or more instruments may be inserted into such working channels for performing a variety of surgical operations. Such instruments may includes but are not limited to scissors, other surgical blades, wire snares, banding devices, surgical laser devices, cauterizers, or any other surgical or diagnostic instrument known in the art and insertable through such a working channel. Thus, if the device 80 includes one

or more working channels, the device 80 may be operated as an endoscopy device as is known in the art.

While the invention has been described with respect to a limited number of embodiments, it will be appreciated that many variations, modifications and
5 other applications of the invention may be made which are within the scope and spirit of the invention.

CLAIMS

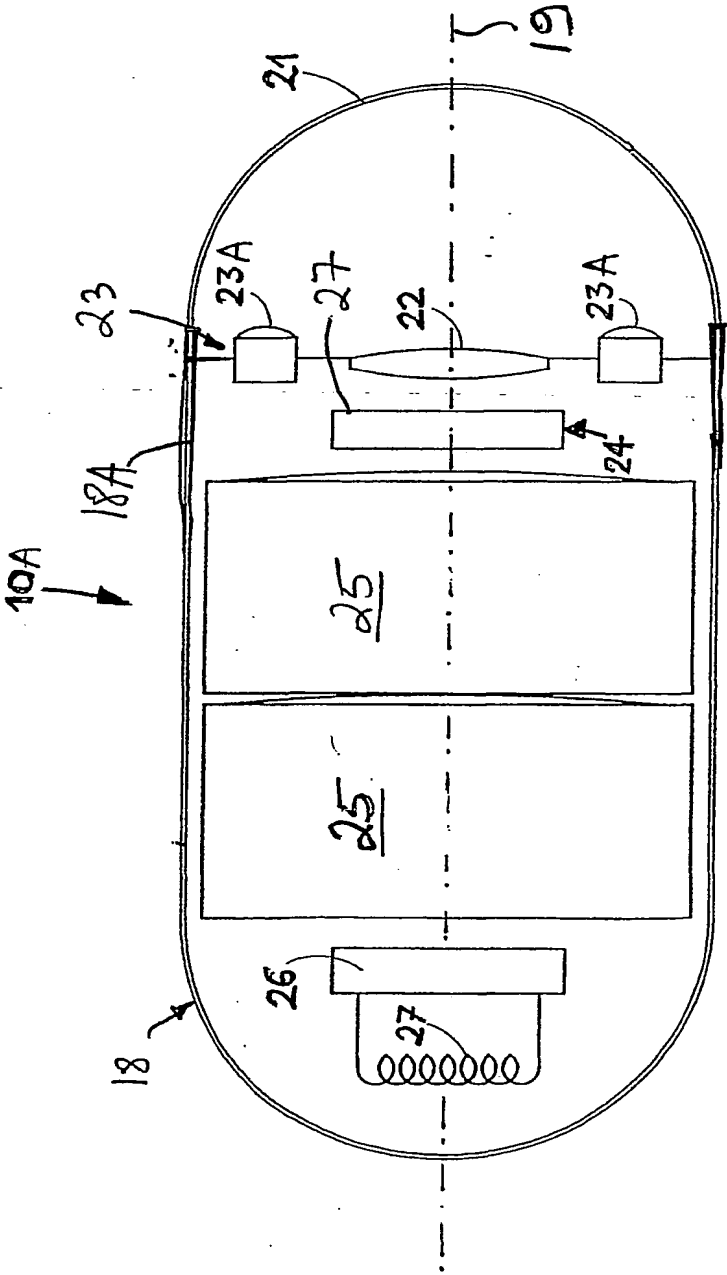
1. A CMOS image sensor comprising a pixel array portion and a circuitry
5 portion, said circuitry portion being longitudinally segregated from the pixel array portion.
2. The CMOS image sensor according to claim 1 wherein the pixel array portion is continuous with the circuitry portion.
3. The CMOS image sensor according to claim 1 wherein the circuitry
10 portion comprises circuitry selected from the group consisting of timing circuitry, analog to digital conversion circuitry, input/output (I/O) circuitry, transmitting circuitry or a combination thereof.
4. An in vivo imaging device comprising:

a housing having a longitudinal axis, said housing accommodating
15 a CMOS image sensor according to claim 1, said image sensor disposed substantially parallel to the longitudinal axis of the housing; and

a light deflecting element positioned to intercept reflected light and configured to deflect the reflected light to the pixel array.

20
5. The device according to claim 4 wherein the light deflecting element is positioned at substantially 45° to the pixel array.
6. The device according to claim 4 wherein the light deflecting element is a mirror.
- 25 7. The device according to claim 4 further comprising an optical system, said system configured for changing an image to compensate for distortion of an image by the light deflecting element.
8. The device according to claim 4 further comprising an aperture for reflected light to progress through.

Fig. 1



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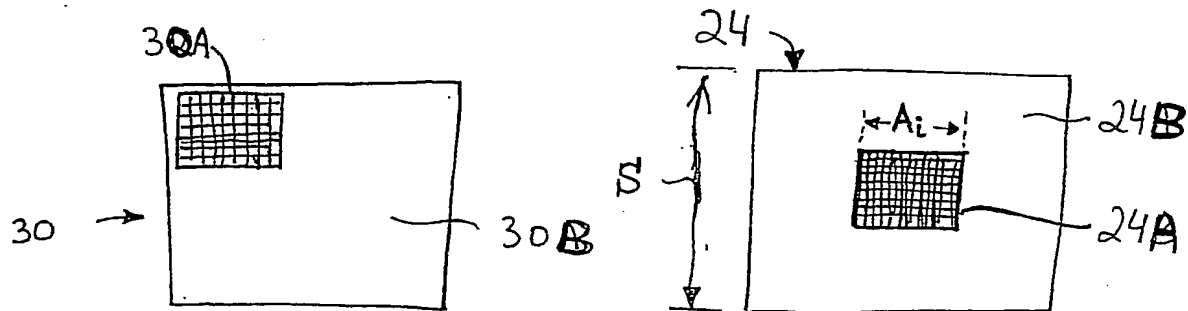


Fig. 2

Fig. 3

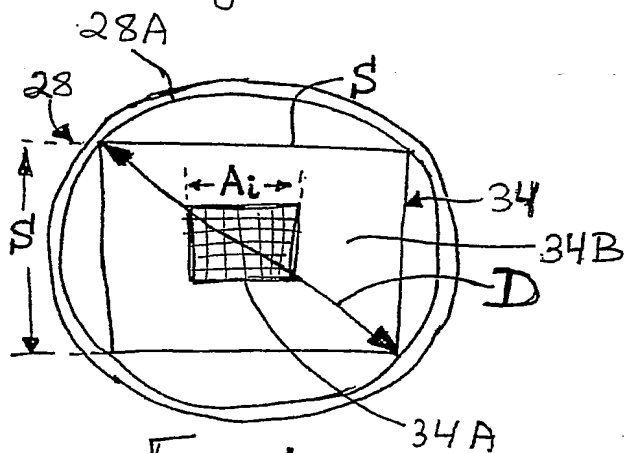


Fig. 4

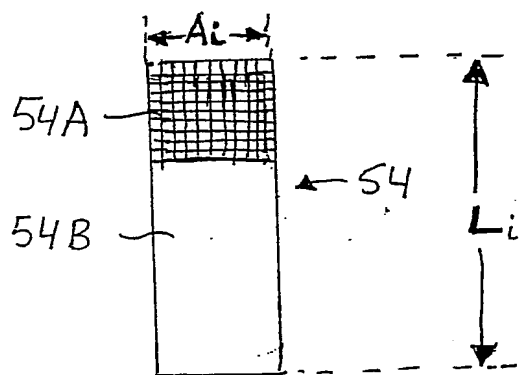


Fig. 5

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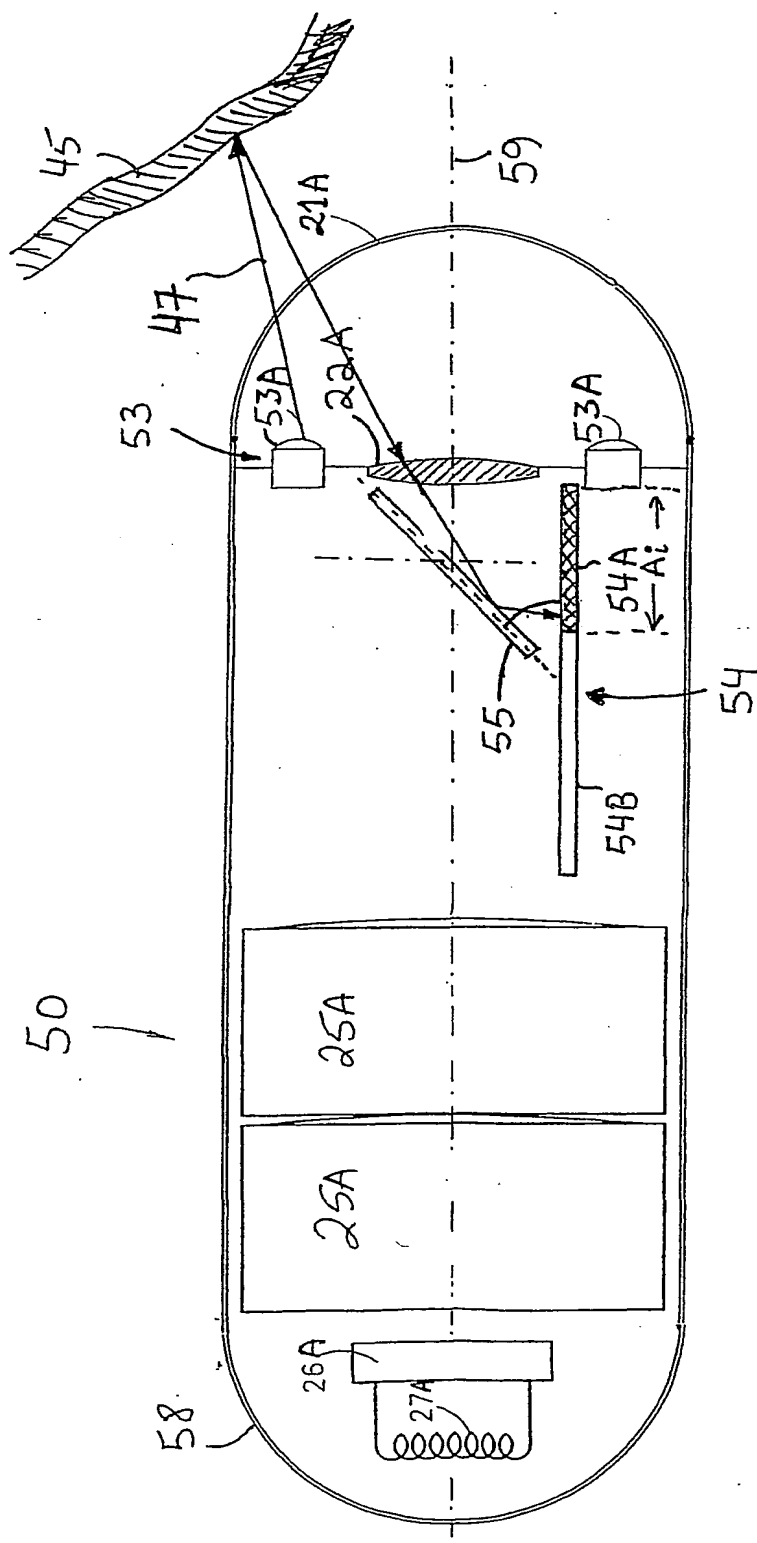


FIG. 6

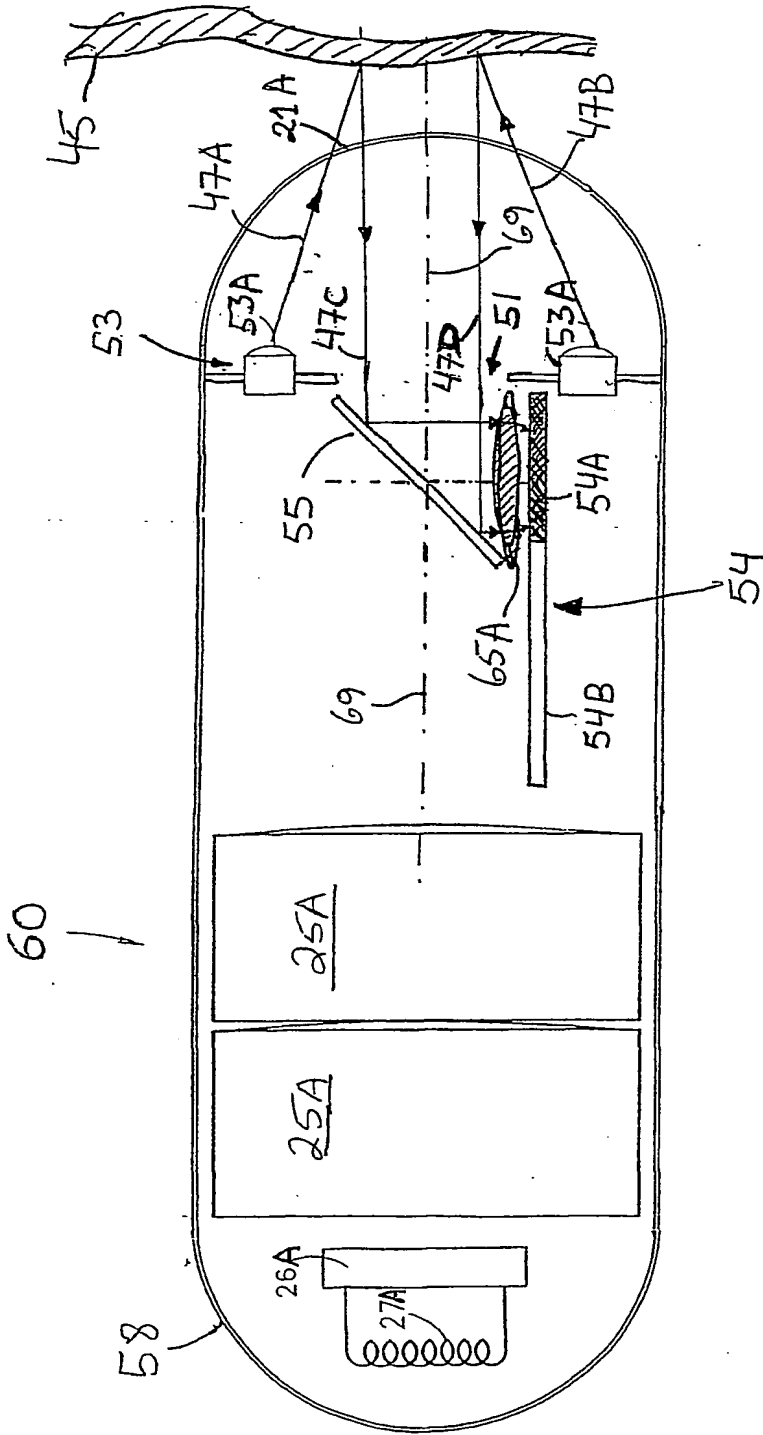


FIG. 7

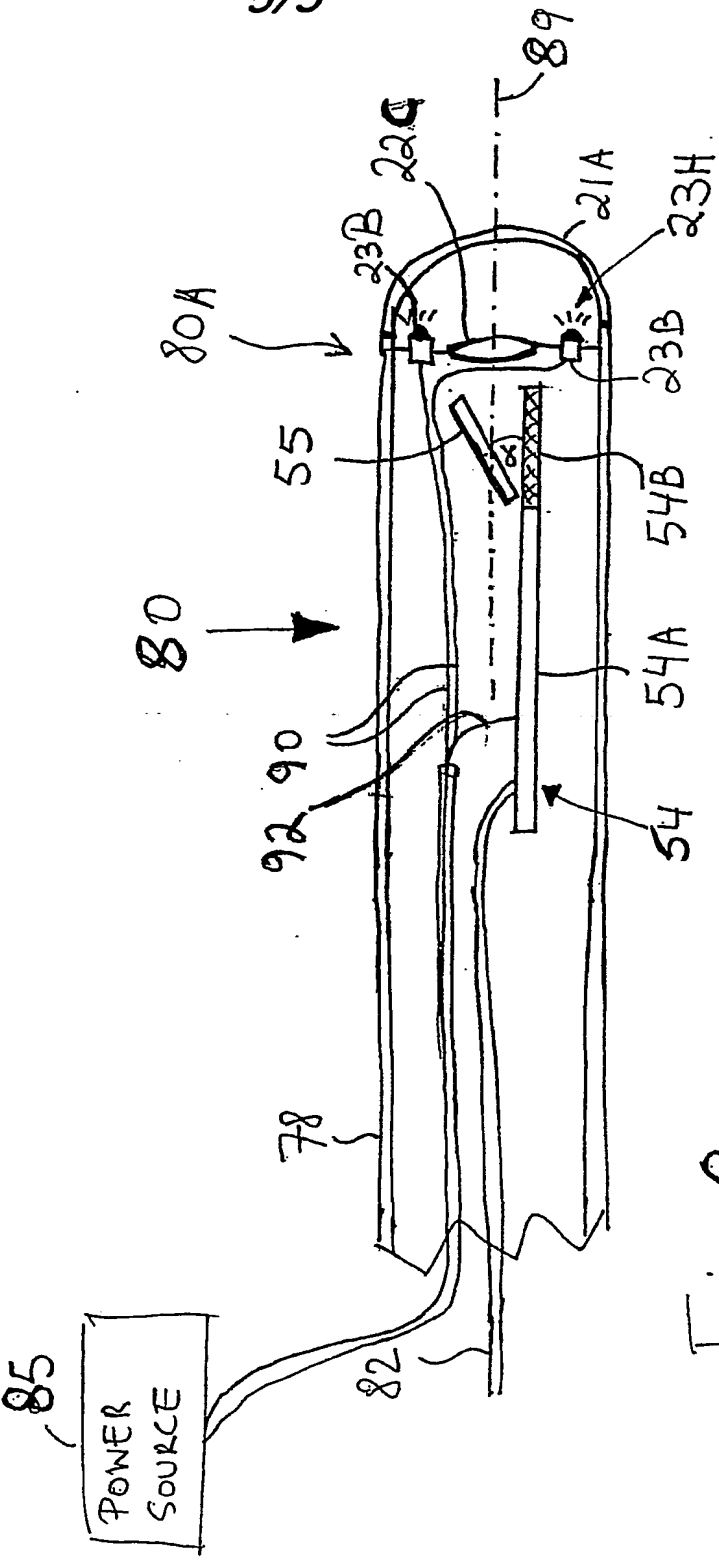


Fig. 8